

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Jeffrey Lukanc

Assignee: Integrated Device Technology, Inc.

Title: TEST BUS ARCHITECTURE FOR EMBEDDED RAM AND METHOD
OF OPERATING SAME

Serial No: 09/887,913

Group No.: 2133

Filed: 6/20/2001

Examiner: Guy Lamarre

Attorney Docket No: IDT-1616

Date: June 8, 2005

Mail Stop Amendment
Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO THIRD OFFICE ACTION

Sir:

Applicant submits the following amendments and remarks
in response to the Office Action dated February 8, 2005. A
Request for Extension of Time, extending the period for
response to June 8, 2005, accompanies the present response.

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